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|  | **Objective** |

Greetings! I am Meher Adbol, final year B.E. undergraduate at Institute of Engineering & Technology, DAVV, Indore in the department of Electronics and Instrumentation Department. I am currently looking for opportunities in Signal Processing, VLSI (design and verification using Verilog HDL) and automation & control (Microcontrollers and PLCs). I have proper skillset for working in these fields and looking forward to learn, contribute and work on projects and research in these fields. Currently working on a project in IET DAVV which comes under the C2S scheme (MEITY, Govt. of India) involving application of signal processing, microcontrollers and FPGAs.

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|  | Education |

## Bachelor of Engineering | Institute of Engineering & Technology, DAVV, Indore

### 2019 – 2023

Branch: Electronics and Instrumentation Engineering [CGPA: 7.79(until 6th semester)]

## SSC | Atomic Energy Central School, Indore

### 2017 – 2019

91.2%: Physics, Chemistry, Mathematics, Computer Science, English [CBSE]

## HSC | Atomic Energy Central School, Indore

### 2007 – 2017

10 CGPA (95%) [CBSE]

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|  | Experience |

## Project Trainee / Internship | RRCAT, Department of Atomic Energy

### June 2022 – August 2022

The project includes a simple 8-bit microprocessor and a digital waveform generator on FPGA using Verilog HDL. The microprocessor has an 8-bit ALU and a register memory for storage of inputs and outputs. Three functions square, triangular and saw-tooth waves are generated in the generator. All the above mentioned programs run on the base clock frequency of the FPGA board. During the project duration I got hands-on experience on PLC and SCADA. Learned the basics of PLC and SCADA and worked with digital and analog input and output modules and performed various controlling operations employing analog and digital signals and controlled with SCADA.

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|  | Projects |

## Implementation of Digital Signal Generator using Raspberry Pi |

### September 2021 – September 2021

Made a digital signal generator capable of generating square, saw- tooth, triangular and sinusoidal waveforms with the help of external analog circuit for interfacing the Raspberry Pi to convert the digital signal to analog signal. All the code was written in Python language. All the outputs can be viewed on Digital Storage Oscilloscope.

## Basic Image Compression Algorithm Implementation using MATLAB|

### February 2022 – April 2022

Based on my team’s study on image compression techniques, we worked on implementation of different stages of JPEG image compression algorithm using MATLAB’s image processing and signal processing toolbox along with computer vision toolbox. JPEG is a lossy form of image compression, i.e., the details lost in compression cannot be recovered when decompressed.

## Implementation of a simple microprocessor and digital waveform generator on FPGA using Verilog Hardware Description Language | RRCAT, Department of Atomic Energy

### June 2022 – August 2022

The details of the project can be found in the report attached.

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|  | Skills |

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| * Digital Circuit Design using HDL * Verilog HDL * FPGA Design * Python (Progate Certified) * Raspberry Pi * MATLAB (Certification) * Signal Processing (MATLAB Certification) * Circuit Simulation | * C / C++ * Arduino * PCB Design using EAGLE * PLC and SCADA * Basics of IoT * Analog and Digital Circuit (Design and testing) * JavaScript (Progate Certified) |

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|  | Activities |

My activities include:

* Cleared GATE 2022 with a score of 362/1000 securing AIR 6743 in EC paper code.
* Cleared RMO (Regional Mathematics Olympiad) (State Level)
* Held the position of Vice Captain during school time and conducted and managed in various co-curricular activities and sports.
* Active participation in sports in school as well as college. Sports activities include badminton, chess, cricket, table-tennis and volleyball.
* Attended workshops: Cybersecurity and Ethical Hacking @ IIT Indore

IoT workshop @ Robotronix Pvt. Ltd.

* Languages known: English (Professional working proficiency)

Hindi (Native or Bilingual proficiency)

Marathi (Native or Bilingual proficiency)